To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)
Send any inquiries to http://www.renesas.com/inquiry.
RENESAS TECHNICAL UPDATE

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<td>Title</td>
<td>M16C/63 Group, M16C/6C Group Easing the Limitation of the Debugging Interface</td>
<td>TN-16C-A186A/E</td>
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<td>Applicable Products</td>
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A 1-wire debugging interface is now available for the E8a emulator.

1. Applicable Products
M16C/63 Group and M16C/6C Group MCUs produced on or after the first week in 2010.
Check the product data code for production year and week.

2. Contents
Previously, the interface between the E8a emulator and MCU was a 2-wire clock asynchronous serial I/O. A 1-wire clock asynchronous serial I/O has been added. Therefore, 1-wire clock asynchronous serial I/O can be selected for the products listed above. Refer to the following documents for more information on the connection method:

3. Date code
The date code is listed in the marking of the package.
Figure 1 shows the marking diagram of the M16C/63 Group. Figure 2 shows the marking diagram of the M16C/6C Group.
For example, “001xxxx” indicates the first week of 2010.
PRQP0100JD-B (100P6F-A), PLQP0100KB-A (100P6Q-A), PLQP0080KB-A (80P6Q-A)

Figure 1: M16C/63 Group Marking Diagram

PTLG0100KA-A (100F0M)

Figure 2: M16C/6C Group Marking Diagram