1. Abstract

This document describes procedures for the following mode transitions, and setting and canceling low current consumption read mode in the M16C/63 Group:

- Transition from high-speed mode to low-speed mode
- Transition from low-speed mode to high-speed mode
- Transition from low-speed mode to low power mode
- Transition from low power mode to low-speed mode
- Setting and canceling low current consumption read mode

2. Introduction

The application example described in this document applies to the following microcomputer (MCU):

- MCU: M16C/63 Group

This application note can be used with other M16C Family MCUs which have the same special function registers (SFRs) as the above group. Check the manual for any modifications to functions. Careful evaluation is recommended before using the sample code described in this application note.
3. Clock Mode Transition Procedure

Figure 3.1 shows the transition procedure between high-speed mode and low power mode. Figure 3.2 to Figure 3.5 show transition procedures for each mode. Figure 3.6 shows the procedure for setting and canceling low current consumption read mode.

Wait time until the main clock oscillation or sub clock oscillation stabilizes varies depending on the oscillation circuit used. Use the wait time recommended by the crystal unit manufacturer.

![Diagram](image-url)
### Transition from High-Speed Mode to Low-Speed Mode

1. Disabling write protection
2. Setting P8_6 and P8_7 to 0 (not pulled high)
3. Setting P8_6 and P8_7 as input ports
4. Selecting XCIN-XCOUT oscillation function
5. Starting oscillating the XCIN clock
6. Waiting until the sub clock stabilizes
7. Selecting sub clock division ratio
8. Switching the main clock to the sub clock
9. Enabling write protection

### Transition from Low-Speed Mode to Low Power Mode

1. Disabling write protection
2. Disabling oscillation stop/restart detect function
3. Stopping the main clock
4. Enabling write protection
Transition from low power mode to low-speed mode

1. Disable write protection
2. Set XIN-XOUT drive capacity to high
3. Start oscillating the main clock
4. Wait until the main clock stabilizes
5. Enable write protection

Figure 3.4 Transition Procedure from Low Power Mode to Low-Speed Mode

Transition from low-speed mode to high-speed mode

1. Disable write protection
2. Set XIN-XOUT drive capacity to high
3. Start oscillating the main clock
4. Wait until the main clock stabilizes
5. Switch to divide-by-8 mode
6. Set to main clock
7. Switch the sub clock to the main clock
8. Enable bits CM16 and CM17 in the CM1 register
9. Switch to no division mode
10. Enable write protection

Figure 3.5 Transition Procedure from Low-Speed Mode to High-Speed Mode
Low current consumption read mode can be used when the CM07 bit in the CM0 register is 1 (sub clock used as CPU clock).

**Figure 3.6 Setting and Canceling Low Current Consumption Read Mode**

- **Low current consumption read mode**
- **Setting procedure**
  - Set the CM07 bit to 1 (sub clock used as a CPU clock)
  - Set the CM05 bit to 1 (main clock oscillator stop)
  - Set the FMR22 bit to 0 and then to 1 (slow read mode enabled) \(^{(1)}\)
  - Set the FMR23 bit to 0 and then to 1 (low current consumption read mode enabled) \(^{(1)}\)
  - Processing in low current consumption read mode
- **Canceling procedure**
  - Set the FMR23 bit to 0 \(^{(1)}\)
  - Set the FMR22 bit to 0 \(^{(1)}\)
  - Completed

**Note:**
1. Do not rewrite bits FMR22 and FMR23 simultaneously.
4. Sample Code

A sample code can be downloaded from the Renesas Electronics website. To download, click “Application Notes” in the left-hand side menu of the M16C Family page.

4.1 Sample Code Operation

The sample code executes functions (1) to (7) below in order. Refer to 4.2 Function Tables for details of each function.

1. CPU initialization
2. Transition from 125 kHz on-chip oscillator mode to high-speed mode
3. Transition from high-speed mode to low-speed mode
4. Transition from low-speed mode to low power mode
5. Setting and canceling low current consumption read mode
6. Transition from low power mode to low-speed mode
7. Transition from low-speed mode to high-speed mode

The settings in the sample code are as follows: Wait time until the main clock stabilizes is approximately 100 ms when the CPU clock is 32.768 kHz, and wait time until the sub clock stabilizes is approximately 1 sec. when the CPU clock is 4 MHz.

4.2 Function Tables

Function Tables for This Document

<table>
<thead>
<tr>
<th>Declaration</th>
<th>void lows_speed_from_highspeed(void)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Outline</td>
<td>Transition from high-speed mode to low-speed mode</td>
</tr>
<tr>
<td>Argument</td>
<td>None</td>
</tr>
<tr>
<td>Variable</td>
<td>None</td>
</tr>
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<td>Returned value</td>
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<td>Function</td>
<td>Switch the CPU clock from high-speed mode to low-speed mode.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Declaration</th>
<th>void highspeed_from_lowspeed(void)</th>
</tr>
</thead>
<tbody>
<tr>
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<td>Transition from low-speed mode to high-speed mode</td>
</tr>
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<td>Argument</td>
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<tr>
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</tr>
<tr>
<td>Function</td>
<td>Switch the CPU clock from low-speed mode to low power mode.</td>
</tr>
</tbody>
</table>
### Declaration

void lowspeed_from_lowpower(void)

#### Outline
Transition from low power mode to low-speed mode

#### Argument
None

#### Variable
None

#### Returned value
None

#### Function
Switch the CPU clock from low power mode to low-speed mode.

### Declaration

void low_current_consumption_read_setup(void)

#### Outline
Setting and canceling low current consumption read mode

#### Argument
None

#### Variable
None

#### Returned value
None

#### Function
Configure settings for low current consumption read mode, execute low_current_consumption_read(), and cancel low current consumption read mode. This function does not include processes to set the CPU clock to low power mode, or restore the CPU clock. Execute this function after setting the CPU clock to low power mode. Then restore the CPU clock.

### Declaration

void low_current_consumption_read(void)

#### Outline
Processing in low current consumption read mode

#### Argument
None

#### Variable
None

#### Returned value
None

#### Function
Called from low_current_consumption_read_setup(). Add a program to be processed in low current consumption read mode.

### Declaration

void mcu_init(void)

#### Outline
CPU initialization

#### Argument
None

#### Variable
None

#### Returned value
None

#### Function
Set to single-chip mode. Switch the CPU clock from 125 kHz on-chip oscillator mode divided-by-8 to 125 kHz on-chip oscillator mode divided-by-1.

### Declaration

void highspeed_from_foco125k(void)

#### Outline
Transition from 125 kHz on-chip oscillator mode to high-speed mode

#### Argument
None

#### Variable
None

#### Returned value
None

#### Function
Switch the CPU clock from 125 kHz on-chip oscillator mode (fOCO-S divided by 1) to high-speed mode.
5. Reference Documents

M16C/63 Group User’s Manual: Hardware (Hardware Manual) Rev.1.00
The latest version can be downloaded from the Renesas Electronics website.

Technical Update/Technical News
The latest information can be downloaded from the Renesas Electronics website.

M16C Series/R8C Family C Compiler Package V.5.45 C Compiler User Manual Rev.2.00
The latest version can be downloaded from the Renesas Electronics website.

Website and Support

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http://www.renesas.com/

Inquiries
http://www.renesas.com/inquiry
# Revision History

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<tr>
<th>Rev.</th>
<th>Date</th>
<th>Description</th>
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</thead>
<tbody>
<tr>
<td>1.00</td>
<td>2010.07.01</td>
<td>First edition issued</td>
</tr>
</tbody>
</table>

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General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this manual, refer to the relevant sections of the manual. If the descriptions under General Precautions in the Handling of MPU/MCU Products and in the body of the manual differ from each other, the description in the body of the manual takes precedence.

1. Handling of Unused Pins
   Handle unused pins in accord with the directions given under Handling of Unused Pins in the manual.
   - The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on
   The state of the product is undefined at the moment when power is supplied.
   - The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
     In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.
     In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses
   Access to reserved addresses is prohibited.
   - The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals
   After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.
   - When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products
   Before changing from one product to another, i.e. to one with a different part number, confirm that the change will not lead to problems.
   - The characteristics of MPU/MCU in the same group but having different part numbers may differ because of the differences in internal memory capacity and layout pattern. When changing to products of different part numbers, implement a system-evaluation test for each of the products.
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